

35474 U.S. PTO  
10/065045  
09/13/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPLICATION		FILING DATE		CLASS	SUBCLASS	GAU	EXAMINER
APPL NUM 10065045		09/13/2002		331	17	2817	Kinthead
**APPLICANTS: Ho Hsu-Feng; Hsu Tse-Hsiang.							
**CONTINUING DATA VERIFIED:							
** FOREIGN APPLICATIONS VERIFIED:							
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials				<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no ATTORNEY DOCKET NO MTKP0019USA			
TITLE : Phase-locked loop having phase detector error signal reshaping and method thereof							
U.S. DEPT. OF COMM. PAT. & TM. PTO-4361 (Rev. 12-94)							

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for C.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drawg.	Figs. Drawg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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